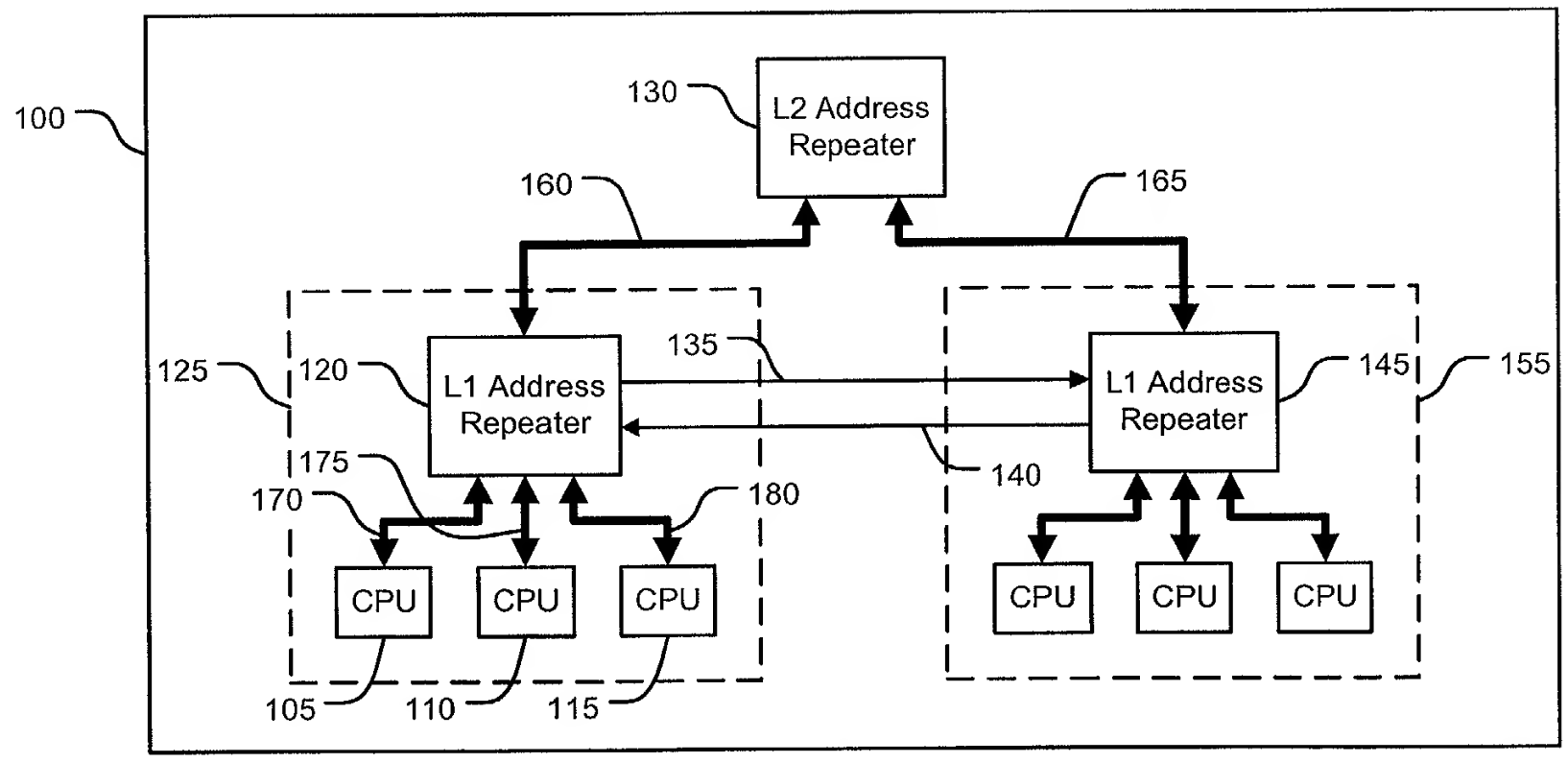
100
130
L2 Address Repeater
160
165
125
120
L1 Address Repeater
135
L1 Address Repeater
145
155
175
170
180
140
CPU
CPU
CPU
CPU
CPU
CPU
105
110
115

Figure 1

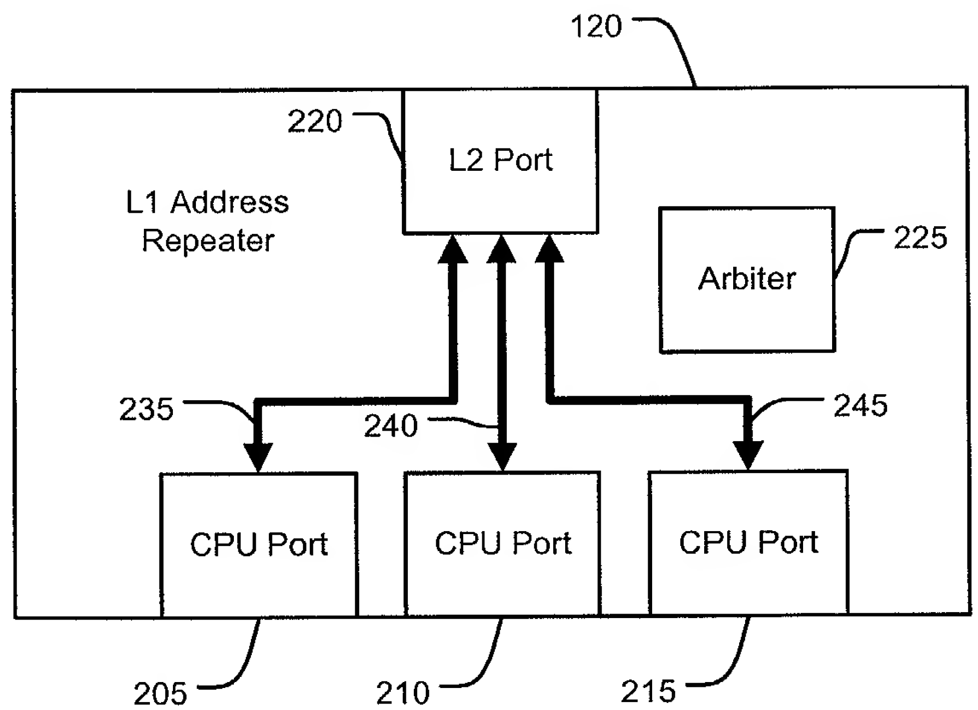

Figure 2

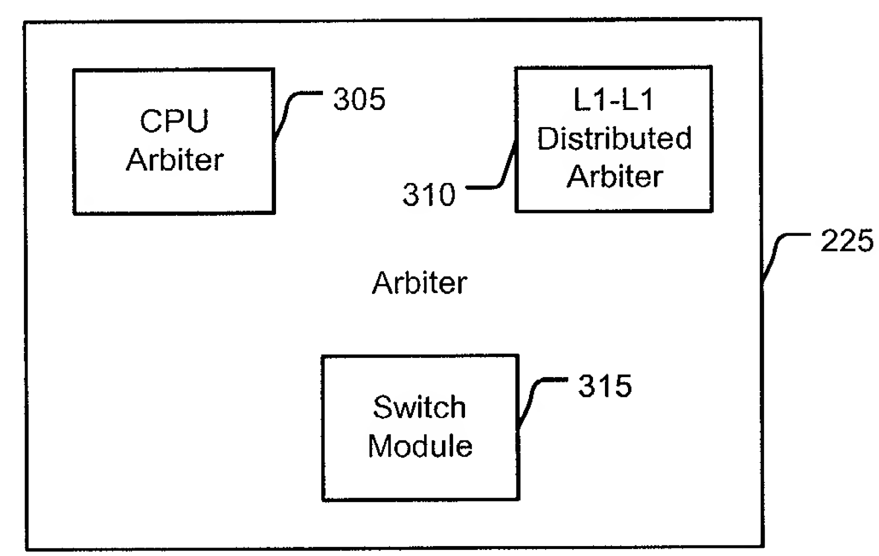

Figure 3

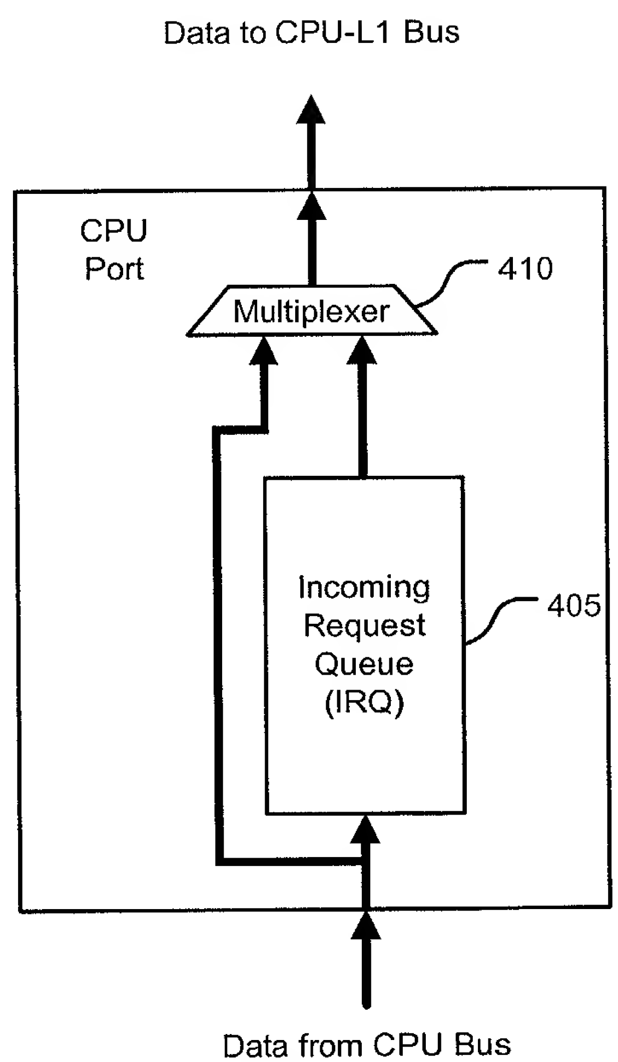

Figure 4(a)

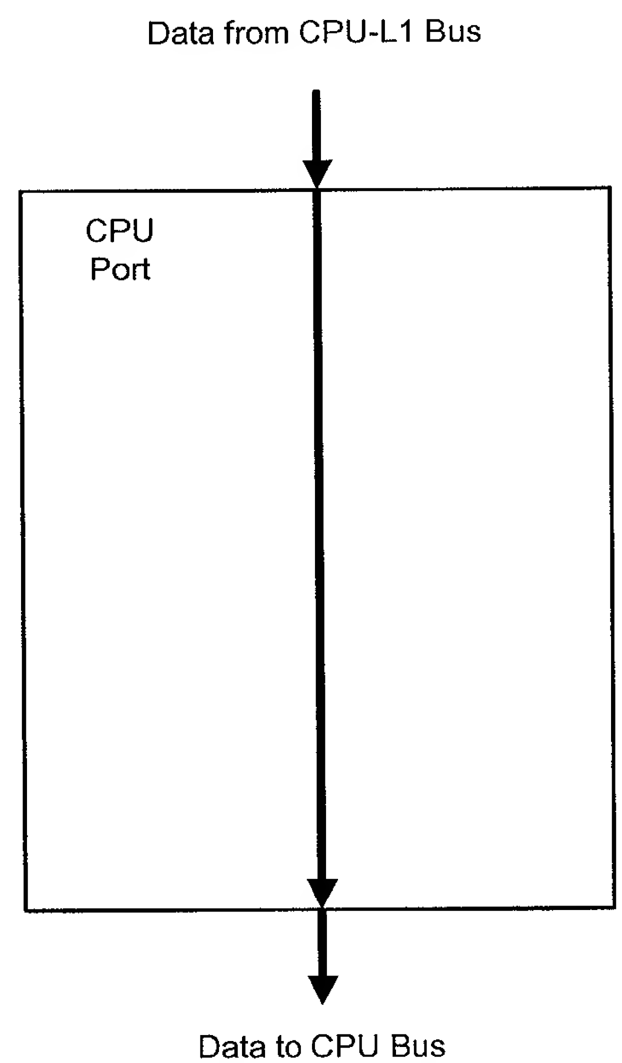

Figure 4(b)

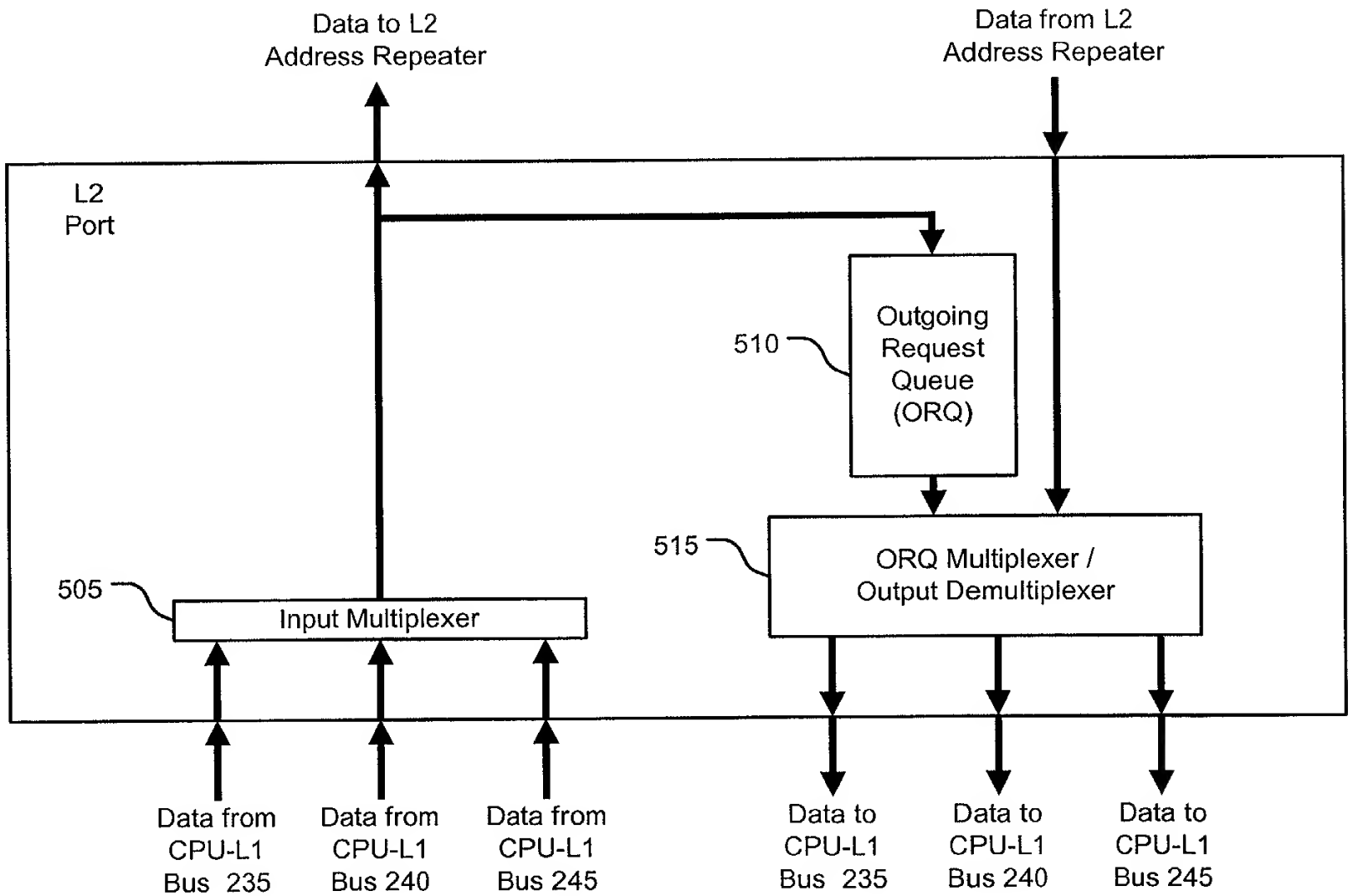

Figure 5

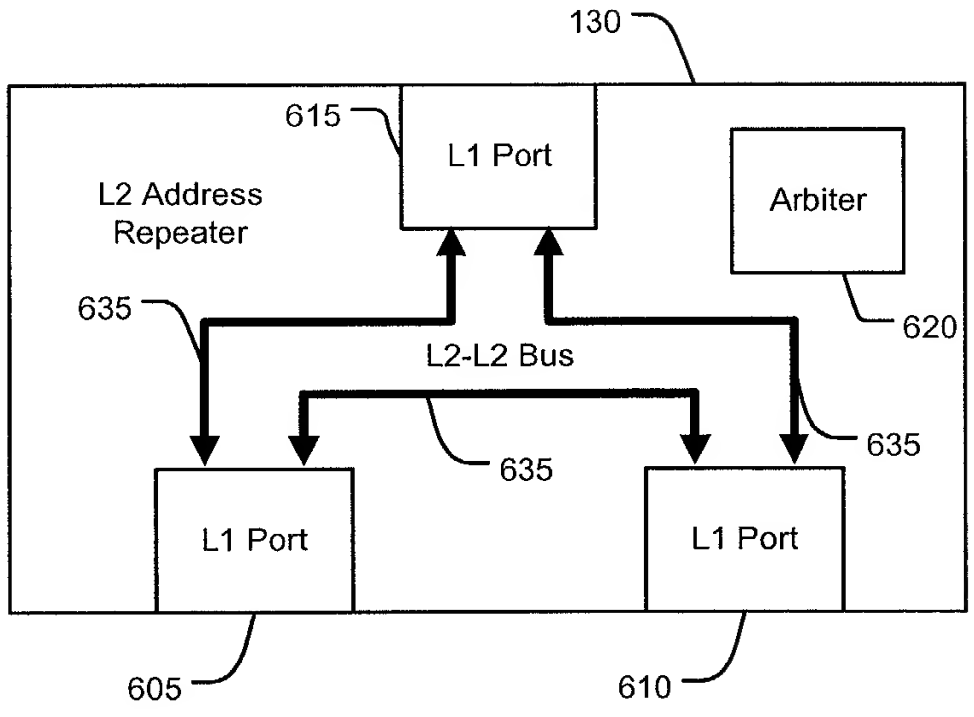
130
615
L1 Port
L2 Address
Repeater
Arbiter
620
635
L2-L2 Bus
635
635
L1 Port
L1 Port
605
610

Figure 6

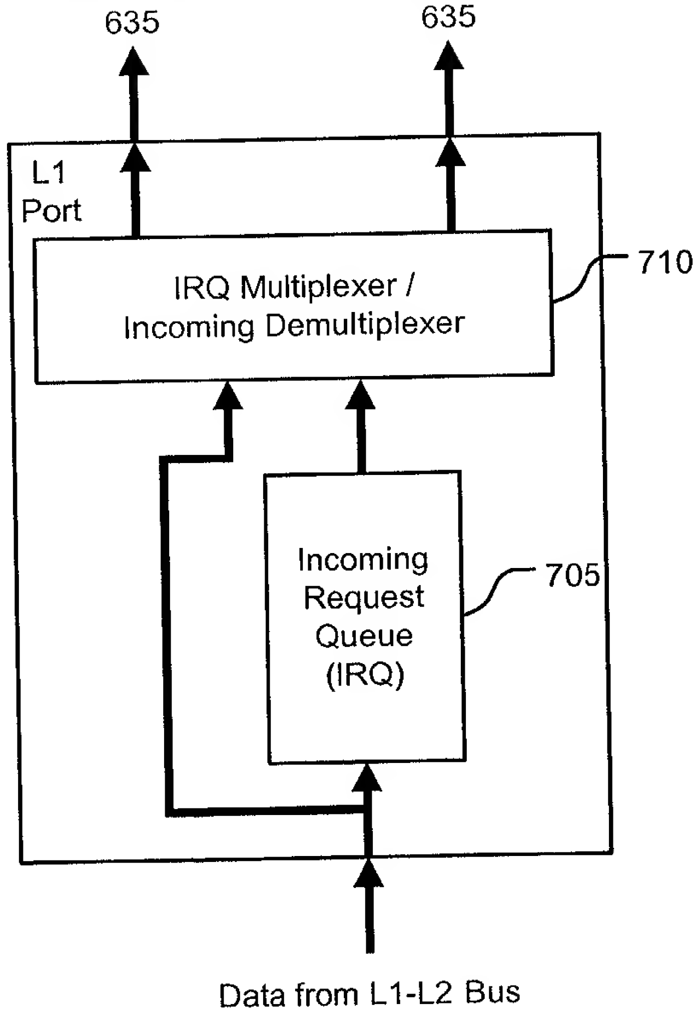

Figure 7(a)

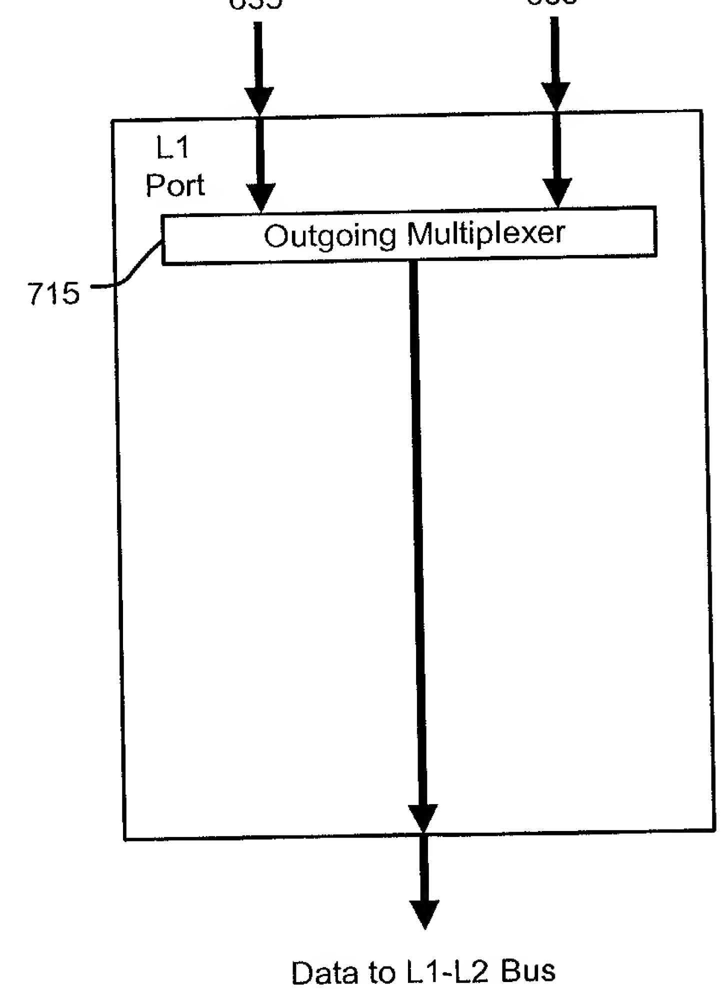

Figure 7(b)

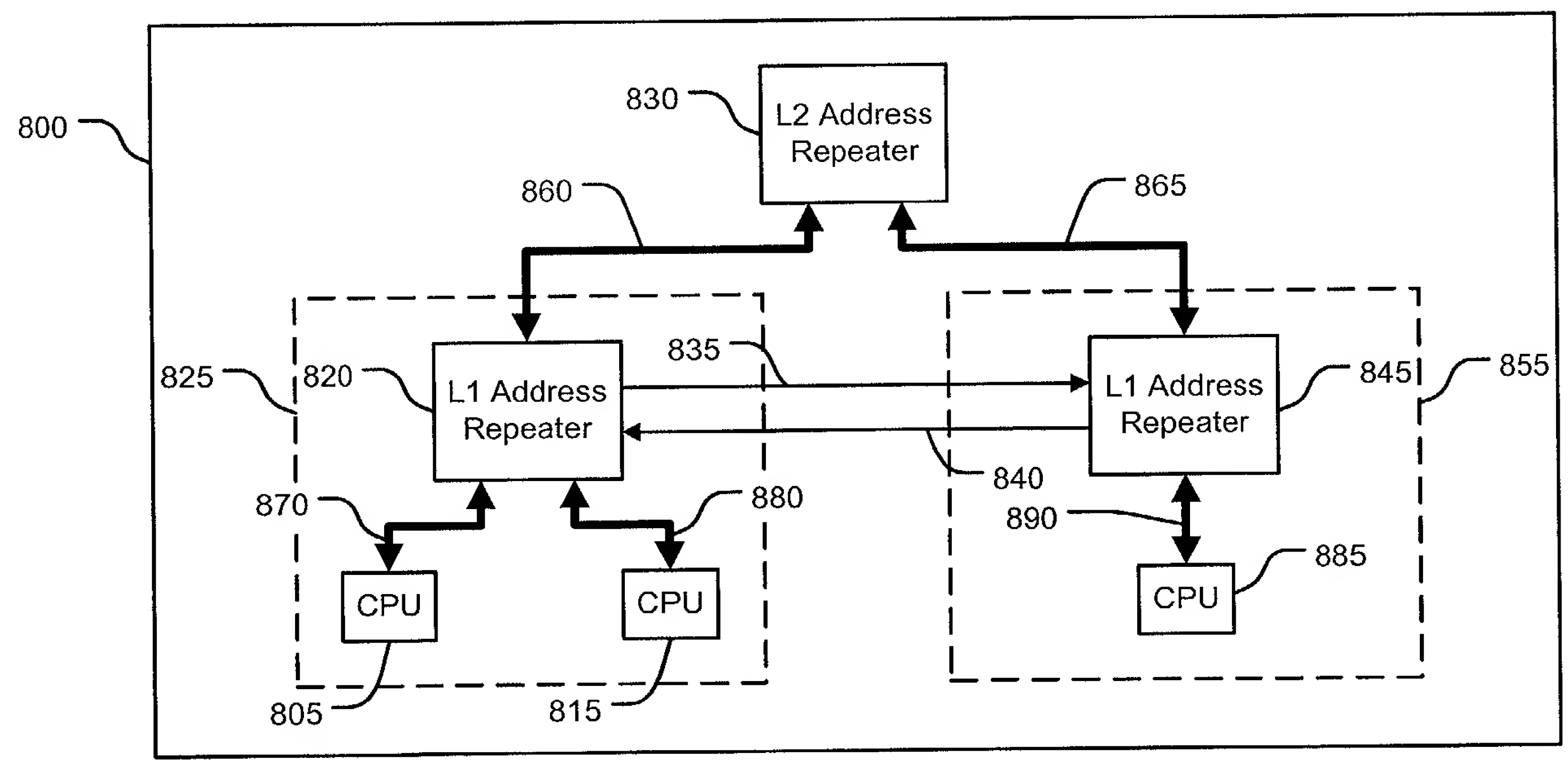

Figure 8

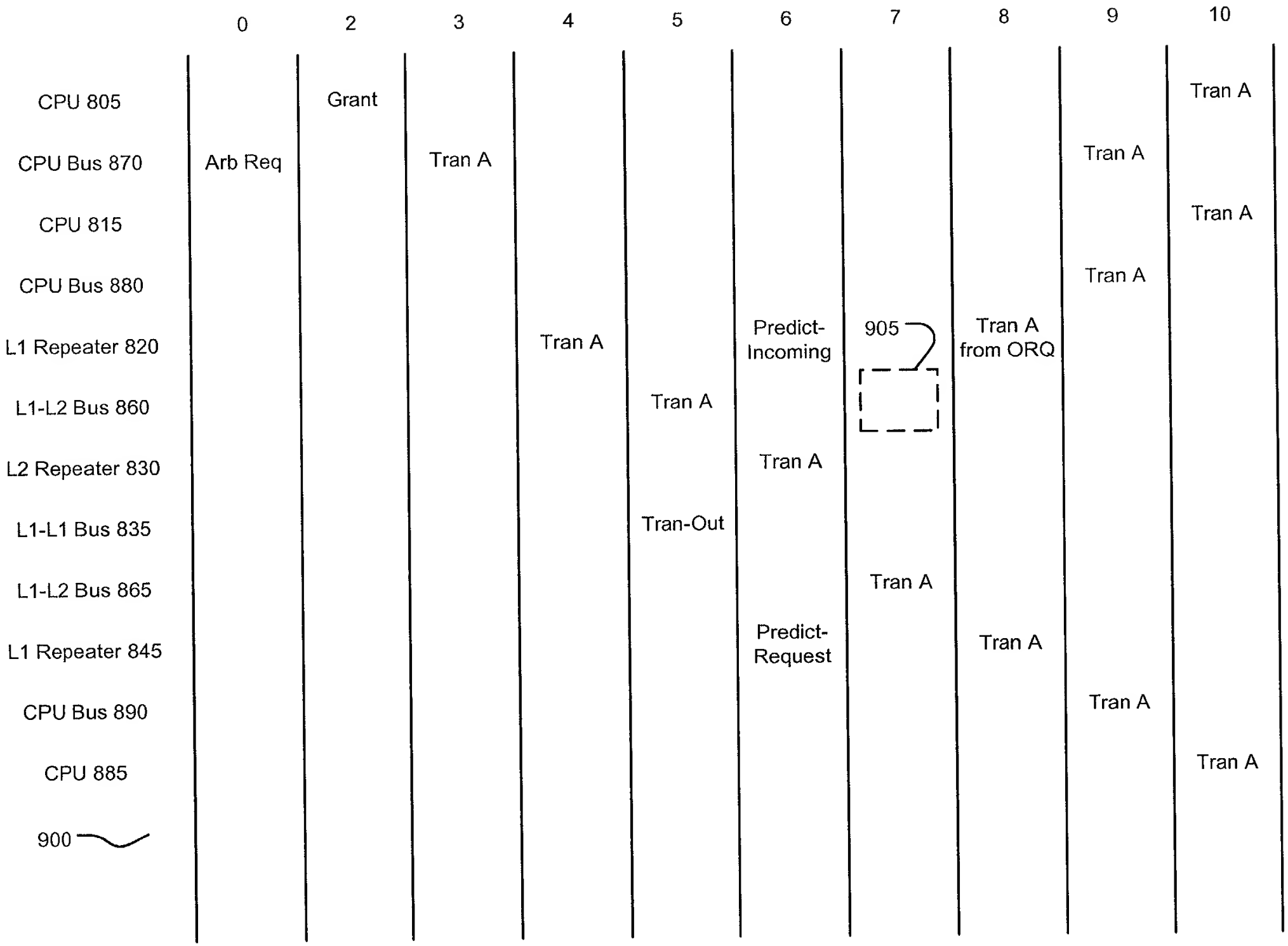
0
2
3
4
5
6
7
8
9
10
CPU 805
CPU Bus 870
CPU 815
CPU Bus 880
L1 Repeater 820
L1-L2 Bus 860
L2 Repeater 830
L1-L1 Bus 835
L1-L2 Bus 865
L1 Repeater 845
CPU Bus 890
CPU 885
900
Arb Req
Grant
Tran A
Tran A
Tran A
Tran-Out
Predict-
Incoming
Tran A
Predict-
Request
905
Tran A
Tran A
Tran A
Tran A
Tran A
Tran A
Tran A
Tran A
Tran A

Figure 9